Substitute Form PTO-1449 (Modified) U.S. Department of Commerce Patent and Trademark Office	Attorney Docket No. MP0280 / 13361-0054001	Application No. 10/631,327
Information Disclosure Statement by Applicant (Use several sheets if necessary)	Applicant Eitan Rosen	
(37 CFR §1.98(b))	Filing Date July 30, 2003	Group Art Unit 2116

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	A1	2005/0015560	1/2005	Bae			
	A2	2006/0017480	1/2006	Lin			
	A3	5,418,822	5/1995	Schlachter et al.			
	A4	6,240,043	5/2001	Hanson et al.			
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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes	Translation No
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Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	A15	Rabacy et al., "Digital Integrated Circuits: a design perspective", 2003, Pearson Education, 2nd ed. pp. 453-459.
	A16	Marvell International Ltd.; Discovery LT MIPS System Controller, MV64420; 2004, 2 pages.
	A17	Micron Technology, Inc.; TN-46-05 General DDR SDRAM Functionality, Technical Note; July 2001; 11 pages.
	A18	Lattice Semiconductor Corporation; Dedicated DDR Memory Interface Circuitry, Mar. 17, 2005; 2 pages.

Examiner Signature	Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.